

a series of values of said received signal serially in delay means (DB'_0 , DB'_1 ...) and outputting previously stored values in parallel as said interpolated signals.

76. (New) A decoding method as claimed in claim 55, wherein the step of demultiplexing the interpolated signals uses first delay means (71A) and second delay means (71B) each for accepting a series of values of the received signal, each delay means having values of the received signal loaded therein serially and, alternately, outputting previously stored values in parallel, the arrangement being such that, when the values of the received signal are being loaded into one of the delay means, the interpolated signal values previously stored in the other delay means are being outputted.

REMARKS

The foregoing amendments remove multiple dependencies and the "characterizing" phrases used in International applications. In addition, minor errors identified in the International Preliminary Examination Report have been corrected and some other clarifying amendments and corrections, which are self-explanatory, have been made.

Respectfully submitted,

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